State Consistencies for Cyber-Physical System Recovery

Fanxin Kong, Syracuse University Oleg Sokolsky, James Weimer, Insup Lee, University of Pennsylvania



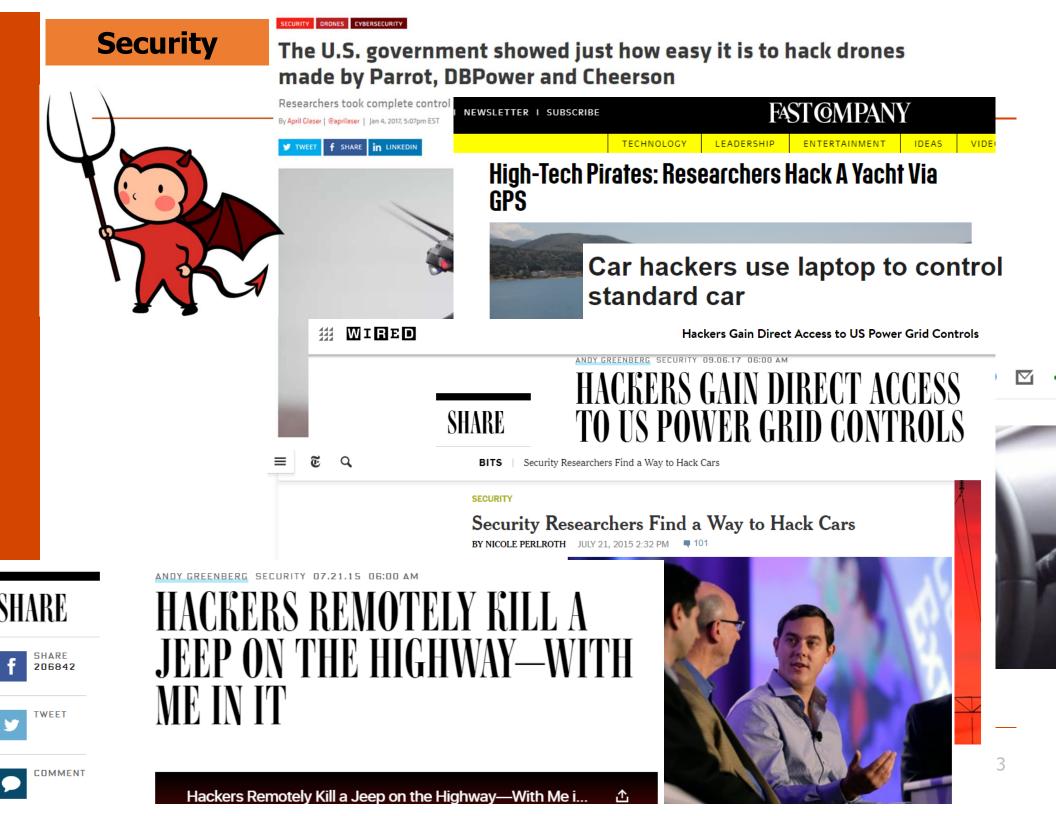
Department of Electrical Engineering and Computer Science

April 15, 2019

Cyber-Physical Systems

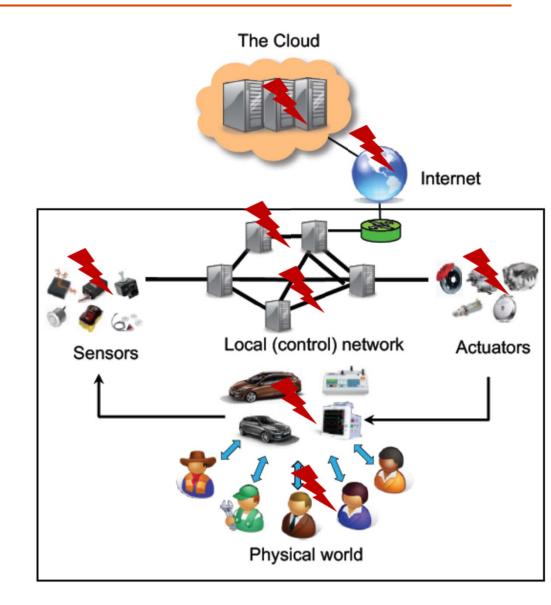


We are living in a Cyber-Physical System world! Syracuse University



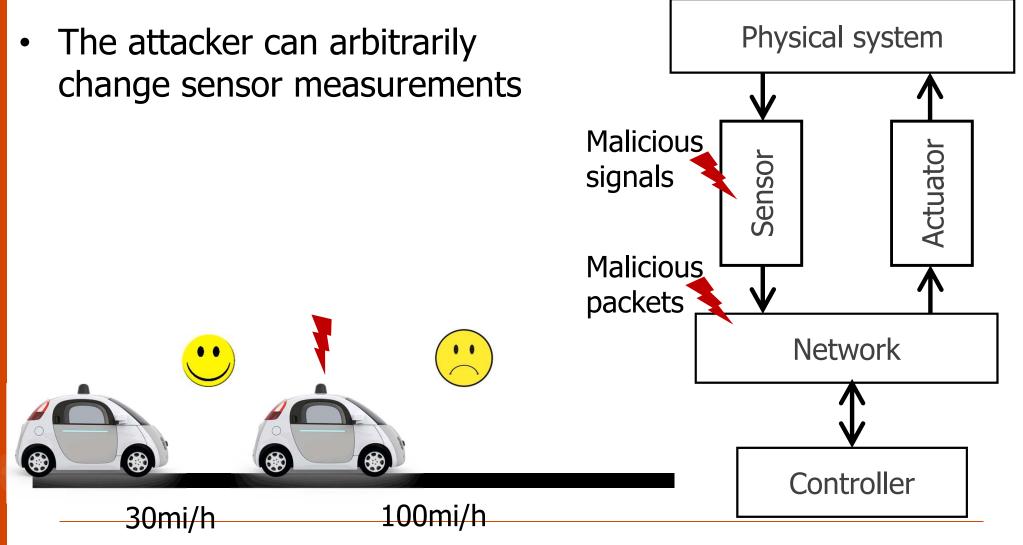
CPS Attack Surfaces

- Cyber attack surfaces
 - e.g., communication, networks, computers, ...
- Environmental attack surfaces
 - e.g., GPS signal, electromagnetic interference, ...
- Physical attack surfaces
 - e.g., locks, casings, cables,
 ...
- Human attack surfaces
 - e.g., phishing, blackmail, ...



What we study and why?

Target: Sensor Attacks



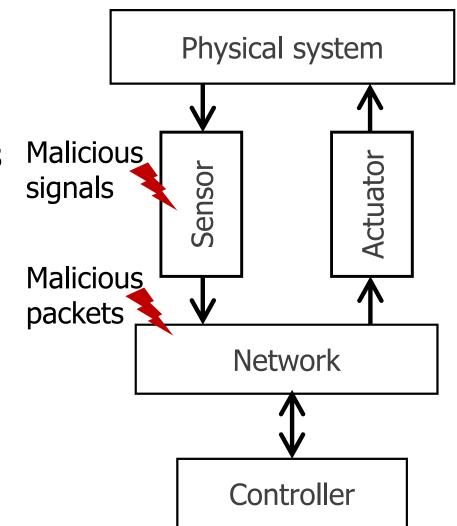
What we study and why?

Target: Sensor Attacks

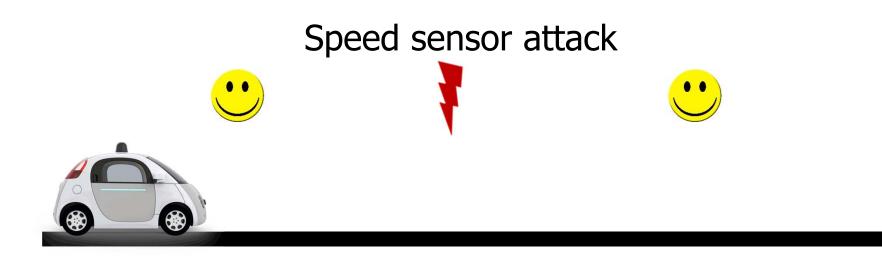
- The attacker can arbitrarily change sensor measurements
 - environmental attack surfaces
 - cyber attack surfaces

Goal: Resilience

• To ensure control performance under sensor attacks







- Ideally, the system performs (almost) the same as if there is no attack
 - Example: cruise control under a speed sensor attack

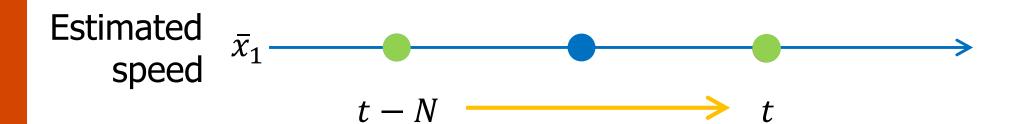
Outline

- Background
- Review on CPS recovery
 - Roll-forward recovery
 - How well does it work
- State consistencies for CPS recovery
 - Consistency definitions
 - Evaluation
- Conclusion



Roll-forward recovery: Rolling the system to the current time by starting from a consistent cyber-physical-state

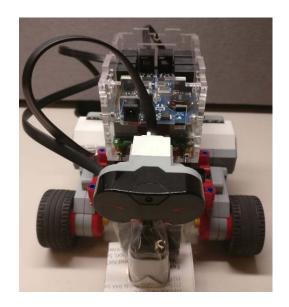
Prediction using historical state



• Example: model-based prediction (ICCPS2018)

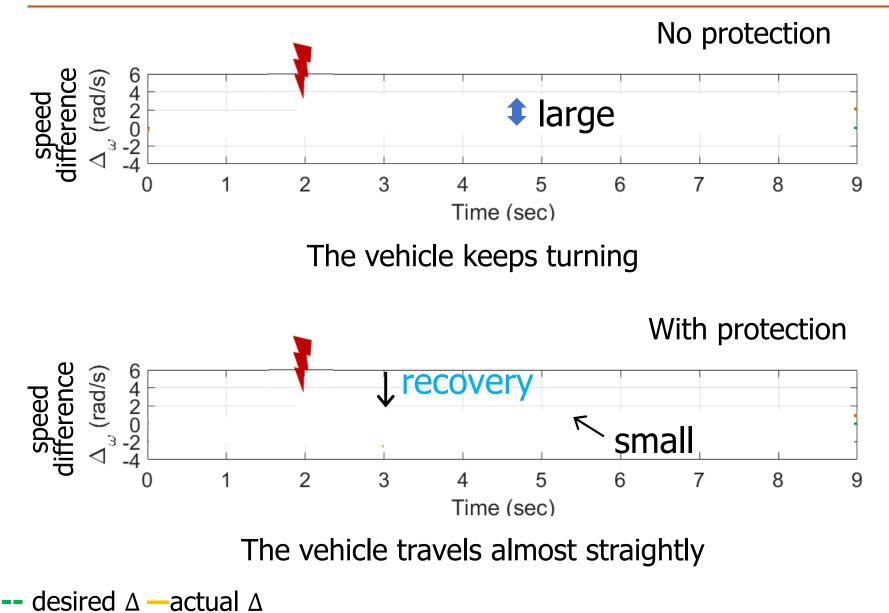
Scenario: travelling in a straight line

- Testbed: an unmanned vehicle. Each front wheel is driven by a motor, and each motor has a speed sensor
- Goal: to keep a vehicle travel in a straight line, i.e., the two front wheels have the same speed



- Controller: a PID controller supervises and controls the speed difference of the two front wheels
- Attack: the attacker modifies a speed sensor's measurements to a constant value

How well does it work?



What kind of states is used?

We use Consistent Cyber-Physical States

- *Cyber-physical states*: the cyber information that reflects physical states
- *Cyber-physical consistency*: whether the physical state can be accurately reflected by the corresponding cyber information

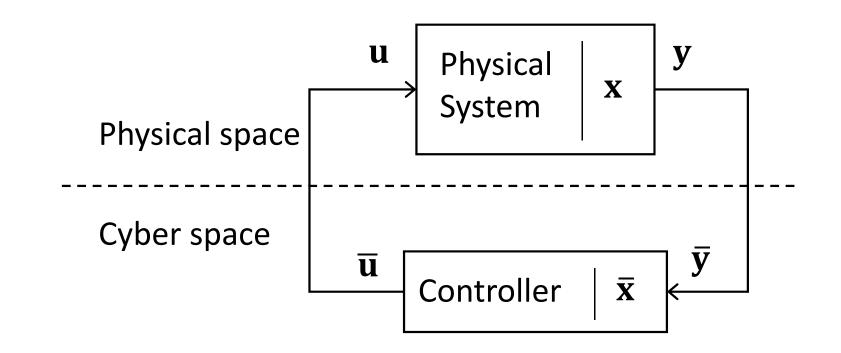
Cyber-physical logic-consistency

Cyber-physical timing-consistency

Synchronization

Freshness

A system diagram of CPS



A cyber-physical state is denoted as $\bar{\mathbf{c}} = \{\bar{\mathbf{x}}, \bar{\mathbf{u}}\}$

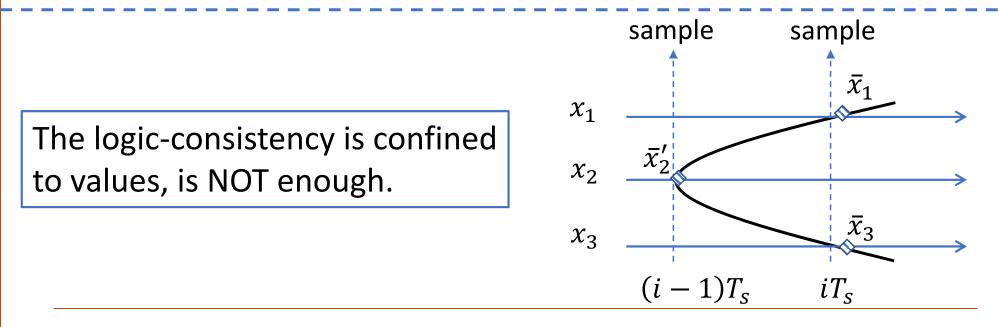
Cyber-Physical Logic-Consistency

DEFINITION 1 (CYBER-PHYSICAL LOGIC-CONSISTENCY). A cyberphysical state $\bar{c} = {\bar{x}, \bar{u}}$ is logic-consistent if

$$\{|\bar{\mathbf{x}} - \mathbf{x}| \le \Delta \mathbf{V}_{\mathbf{x}}\}\tag{1}$$

$$\wedge \{ |\bar{\mathbf{u}} - \mathbf{u}| \le \Delta \mathbf{V}_{\mathbf{u}} \},\tag{2}$$

where ΔV_x and ΔV_u denote the given estimation error and actuation error, respectively, that a system can tolerate.



Cyber-Physical Timing-Consistency

DEFINITION 2 (CYBER-PHYSICAL TIMING-CONSISTENCY). A cyberphysical state $\bar{c} = {\bar{x}, \bar{u}}$ is timing-consistent if it satisfies

(1) Syn-Timing-Consistency:

$$\{ \max_{\forall i} t(\bar{x}_i) - \min_{\forall j} t(\bar{x}_j) | \le \Delta T_x \}$$
(3)

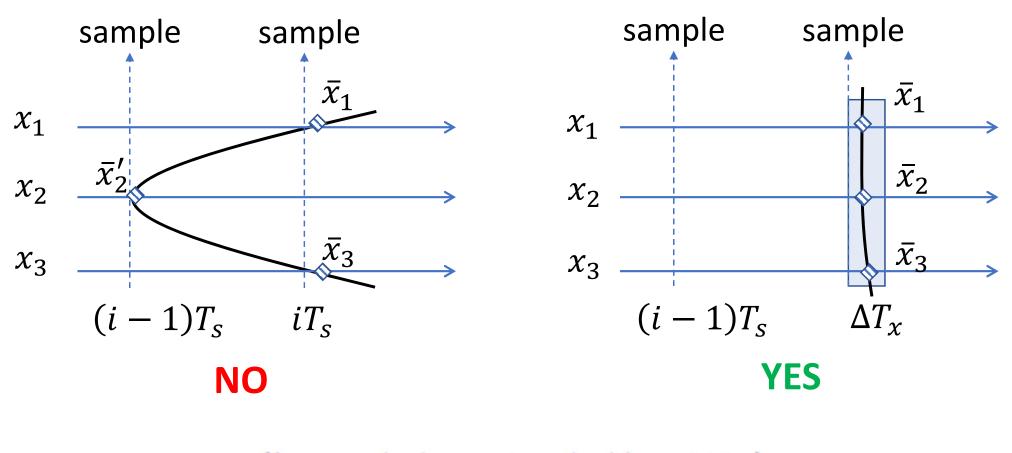
$$\wedge \{ |\max_{\forall j} t(\bar{u}_j) - \min_{\forall i} t(\bar{x}_i)| \le T_s \},$$
(4)

where ΔT_x denotes the maximum difference of states' time stamps that a system can tolerate; T_s is the sampling period. (2) Exp-Timing-Consistency:

$$q(\bar{\mathbf{c}}) \ge h,\tag{5}$$

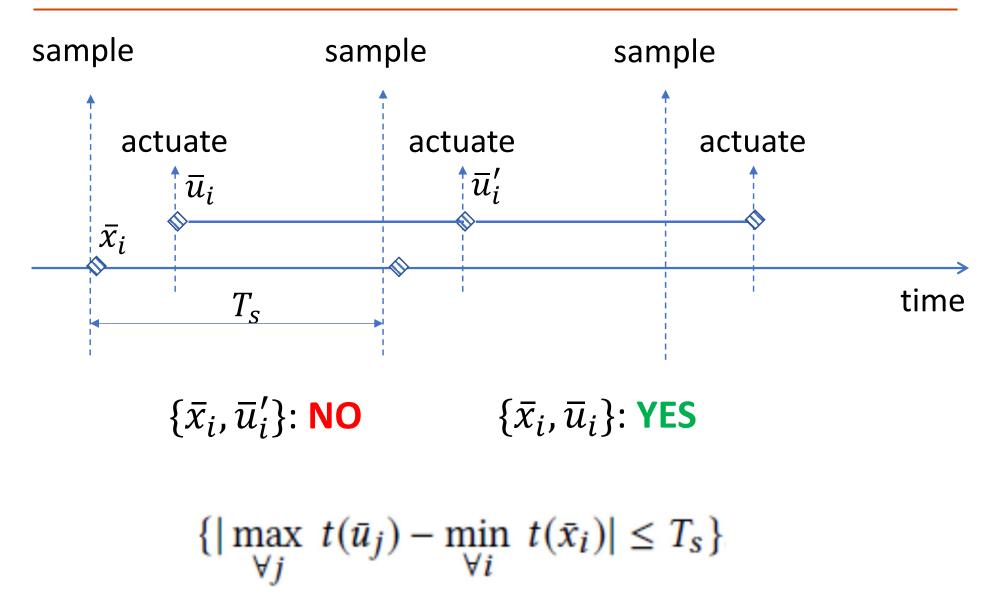
where $q(\cdot)$ is the expire time of a cyber-physical state and h denotes the current time.

(1) Syn-Timing-Consistency (1/2)

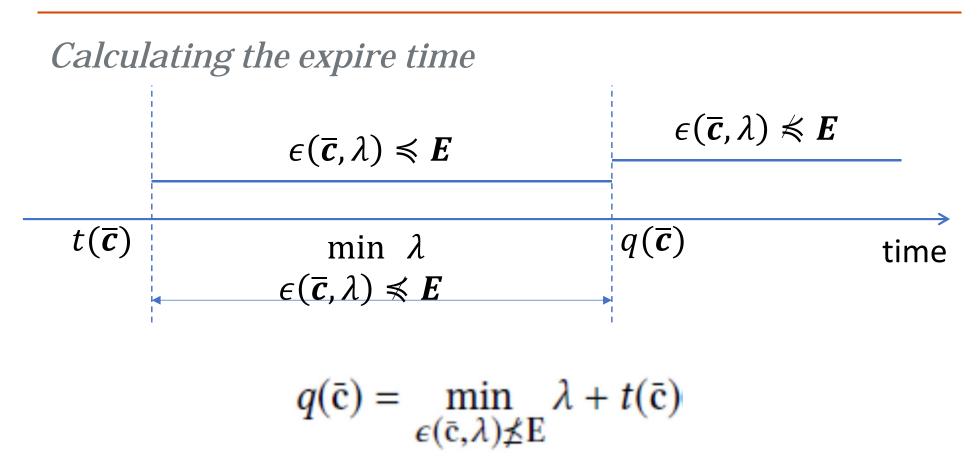


$$\{|\max_{\forall i} t(\bar{x}_i) - \min_{\forall j} t(\bar{x}_j)| \le \Delta T_x\}$$

(1) Syn-Timing-Consistency (2/2)



(2) Exp-Timing-Consistency

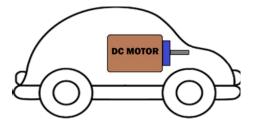


The error of state prediction is unacceptable

Evaluation

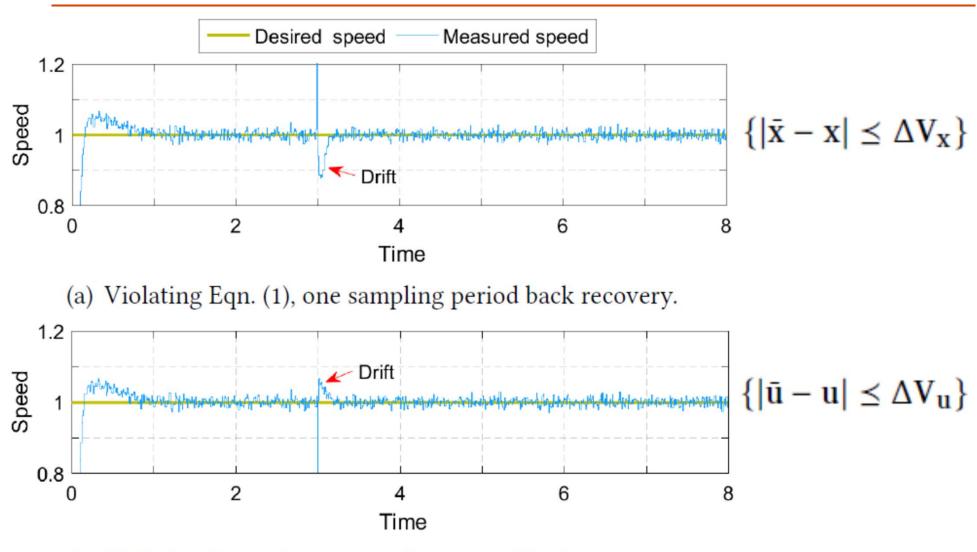
- <u>Goal</u>: to keep a vehicle travel at a constant speed
- <u>Simulator</u>: DC motor speed control using PID controller

$$\begin{bmatrix} i\\ \dot{\omega} \end{bmatrix} = \begin{bmatrix} -\frac{R}{L} & -\frac{K_b}{L}\\ \frac{K_m}{J} & -\frac{K_f}{J} \end{bmatrix} \begin{bmatrix} i\\ \omega \end{bmatrix} + \begin{bmatrix} \frac{1}{L}\\ 0 \end{bmatrix} v$$



• <u>Scenario</u>: an attack is found out and the system performs recovery ONCE to predict the current state

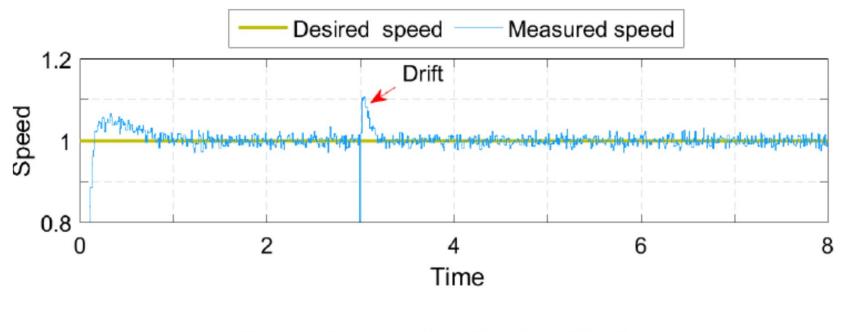
Violating Logic-Consistency



(b) Violating Eqn. (2), one sampling period back recovery.

Violating Syn-Timing-Consistency

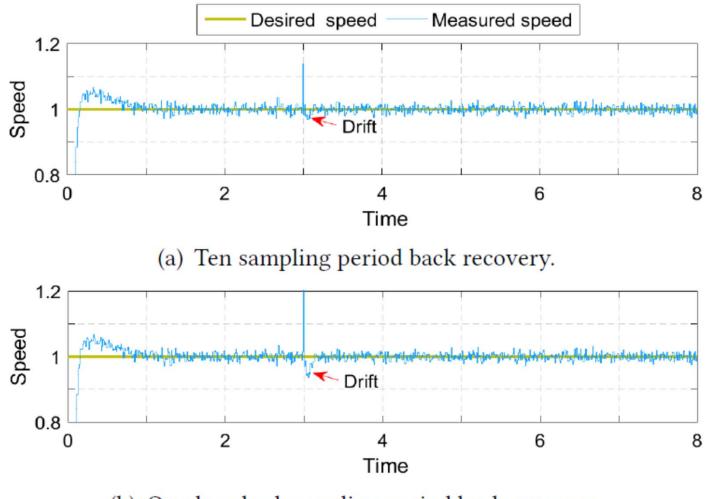
Current (i) and speed (\omega) have different time stamps



 $\{|\max_{\forall i} t(\bar{x}_i) - \min_{\forall j} t(\bar{x}_j)| \le \Delta T_x\}$

Need of Exp-Timing-Consistency

Using older states for recovery resulting in larger drifts



(b) One hundred sampling period back recovery.

Conclusion

- Review on CPS recovery
 - Model-based roll-forward recovery
 - How well does it work
- State consistencies for CPS recovery
 - Defined logic and timing consistencies
 - Why the consistencies is needed

Thank you!